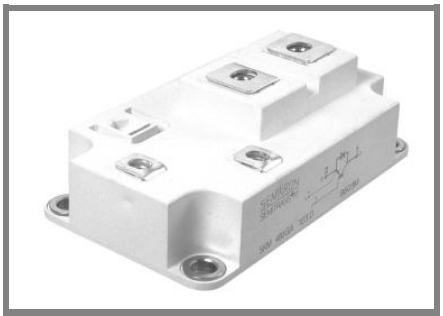
SKM 500GA128D



SEMITRANS® 4

SPT IGBT Modules

SKM 500GA128D

Features

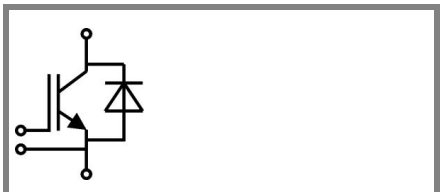
- SPT = Soft-Punch-Through technology
- V_{CEsat} with positive temperature coefficient
- High short circuit capability, self limiting to 6 x I_C

Typical Applications

- AC inverter drives
- UPS
- Electronic welders at f_{sw} up to 20 kHz

Remarks

- $I_{DC} \leq 500$ A for $T_{Terminal} = 100$ °C



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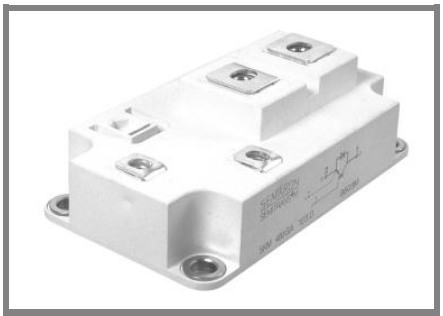
Absolute Maximum Ratings	T_c = 25 °C, unless otherwise specified			
Symbol	**Conditions**		**Values**	**Units**
IGBT				
V_{CES}	T_j = 25 °C		1200	V
I_C	T_j = 150 °C	T_c = 25 °C	700	A
		T_c = 80 °C	500	A
I_{CRM}	I_{CRM}=2xI_{Cnom}		800	A
V_{GES}			± 20	V
t_{psc}	V_{CC} = 600 V; $V_{GE} \leq$ 20 V; V_{CES} < 1200 V	T_j = 125 °C	10	µs
Inverse Diode				
I_F	T_j = 150 °C	T_{case} = 25 °C	530	A
		T_{case} = 80 °C	350	A
I_{FRM}	I_{FRM}=2xI_{Fnom}		800	A
I_{FSM}	t_p = 10 ms; sin.	T_j = 150 °C	3600	A
Module				
$I_{t(RMS)}$			500	A
T_{vj}			- 40... + 150	°C
T_{stg}			- 40... + 125	°C
V_{isol}	AC, 1 min.		4000	V

Characteristics	T_c = 25 °C, unless otherwise specified					
Symbol	**Conditions**		**min.**	**typ.**	**max.**	**Units**
IGBT						
$V_{GE(th)}$	V_{GE} = V_{CE}, I_C = 16 mA		4,5	5,5	6,5	V
I_{CES}	V_{GE} = 0 V, V_{CE} = V_{CES}	T_j = 25 °C		0,2	0,6	mA
V_{CE0}		T_j = 25 °C		1	1,15	V
		T_j = 125 °C		0,9	1,05	V
r_{CE}	V_{GE} = 15 V	T_j = 25°C		2,3	3	mΩ
		T_j = 125°C		3	3,8	mΩ
$V_{CE(sat)}$	I_{Cnom} = 400 A, V_{GE} = 15 V	T_j = 25°C$_{chiplev.}$		1,9	2,35	V
		T_j = 125°C$_{chiplev.}$		2,1	2,55	V
C_{ies}	V_{CE} = 25, V_{GE} = 0 V	f = 1 MHz		35		nF
C_{oes}				4		nF
C_{res}				4		nF
Q_G	V_{GE} = -8V ...+20V			4850		nC
R_{Gint}	T_j = °C			1		Ω
$t_{d(on)}$	R_{Gon} = 4 Ω	V_{CC} = 600V		130		ns
t_r		I_C= 400A		80		ns
E_{on}		T_j = 125 °C		37		mJ
$t_{d(off)}$	R_{Goff} = 4 Ω	V_{GE} = -15V		880		ns
t_f				80		ns
E_{off}				48		mJ
$R_{th(j-c)}$	per IGBT				0,047	K/W

SKM 500GA128D



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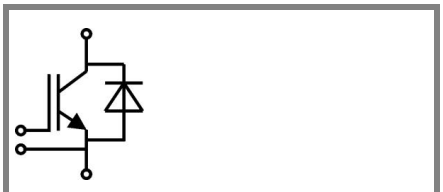

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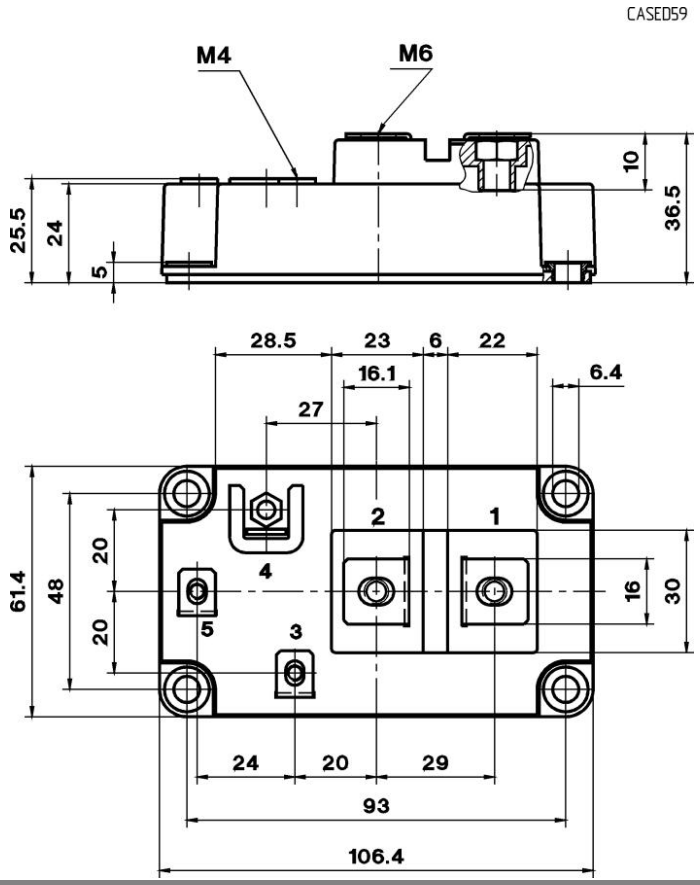
Characteristics

Symbol	Conditions		min.	typ.	max.	Units
Inverse Diode						
$V_F = V_{EC}$	I_{Fnom} = 400 A; V_{GE} = 0 V	T_j = 25 °$C_{chiplev.}$		2	2,5	V
		T_j = 125 °$C_{chiplev.}$		1,8		V
V_{F0}		T_j = 25 °C		1,1	1,2	V
r_F		T_j = 25 °C		2,3	3,3	mΩ
I_{RRM}	I_F = 400 A	T_j = 125 °C		430		A
Q_{rr}	di/dt = 6000 A/µs			67		µC
E_{rr}	V_{GE} = -15 V; V_{CC} = 600 V			29		mJ
$R_{th(j-c)D}$	per diode				0,09	K/W
Module						
L_{CE}				15	20	nH
$R_{CC'+EE'}$	res., terminal-chip	T_{case}= 25 °C		0,18		mΩ
		T_{case}= 125 °C		0,22		mΩ
$R_{th(c-s)}$	per module				0,038	K/W
M_s	to heat sink M6		3		5	Nm
M_t	to terminals M6 (M4)		2,5 (1,1)		5 (2)	Nm
w					330	g

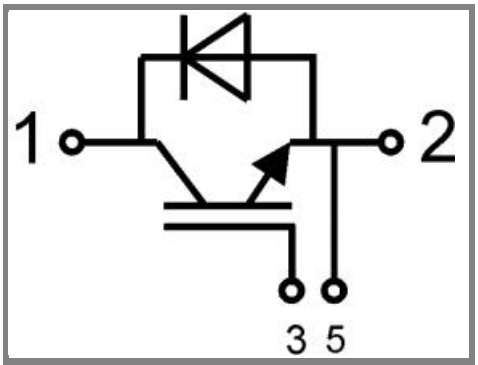
This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, Chapter IX.

This technical information specifies semiconductor devices but promises no characteristics. No warranty or guarantee expressed or implied is made regarding delivery, performance or suitability.

UL Recognized File 63 532



Case D 59



GA Case D 59

