10MQ100NPbF

Vishay High Power Products

Schottky Rectifier, 2.1 A

FEATURES

- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION

The 10MQ100NPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	DC	2.1	А		
V _{RRM}		100	V		
I _{FSM}	$t_p = 5 \ \mu s \ sine$	120	А		
V _F	1.5 Apk, T _J = 125 °C	0.68	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	10MQ100NPbF	UNITS	
Maximum DC reverse voltage V _R		100	V	
Maximum working peak reverse voltage	V _{RWM}	100	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 4	I _{F(AV)}	50 % duty cycle at T_L = 126 °C, rectangular waveform On PC board 9 mm ² island (0.013 mm thick copper pad area)		1.5	A
Maximum peak one cycle non-repetitive surge current, T _J = 25 °C	I _{FSM}	5 μ s sine or 3 μ s rect. pulse	Following any rated load condition and with rated V _{RRM} applied	120	A
See fig. 6		10 ms sine or 6 ms rect. pulse		30	
Non-repetitive avalanche energy	E _{AS}	$T_{J} = 25 \text{ °C}, I_{AS} = 0.5 \text{ A}, L = 8 \text{ mH}$		1.0	mJ
Repetitive avalanche current	I _{AR}			0.5	А

* Pb containing terminations are not RoHS compliant, exemptions may apply







PRODUCT SUMMARY			
I _{F(AV)}	2.1 A		
V _R	100 V		



RoHS

COMPLIAN'

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1	V _{FM} ⁽¹⁾	1 A		0.78	v
		1.5 A		0.85	
		1 A	— Т _J = 125 °С	0.63	
		1.5 A	1) = 125 0	0.68	
Maximum reverse leakage current See fig. 2	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated V _B	0.1	mA
		T _J = 125 °C	V _R = haleu V _R	1	
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.52	V
Forward slope resistance	r _t			78.4	mΩ
Typical junction capacitance	CT	$V_R = 10 V_{DC}$, $T_J = 25 \text{ °C}$, test signal = 1 MHz		38	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	°C/W
			0.07	g
Approximate weight			0.002	oz.
Marking device		Case style SMA (similar D-64)	V	IJ

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink



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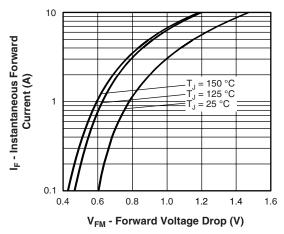
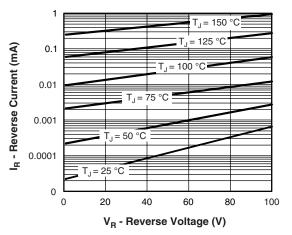
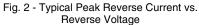


Fig. 1 - Maximum Forward Voltage Drop Characteristics





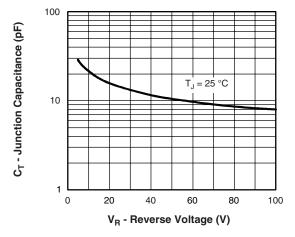
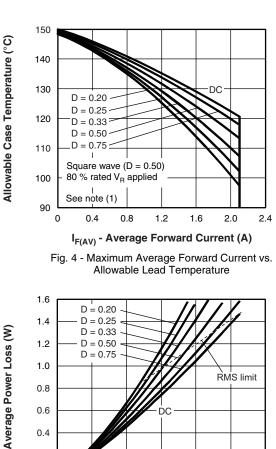


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



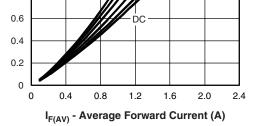
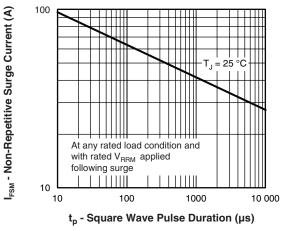
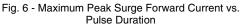


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current





Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

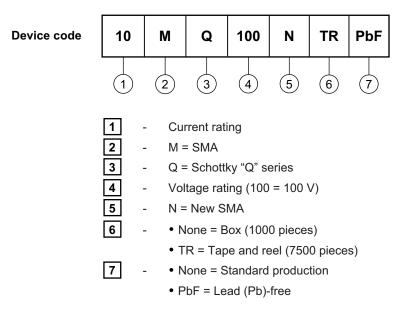
 $Pd = Forward power loss = V_{P(AV)} \times V_{FM} at (I_{F(AV)}/D) \text{ (see fig. 6); } Pd_{REV} = Inverse power loss = V_{P1} \times I_{P1} (1 - D); I_{P1} at V_{P1} = 80 \% rated V_{P1} V_{P1} (1 - D); I_{P1} at V_{P1} = 80 \% rated V_{P1} (1 - D); I_{P1} at V_{P1} = 80 \% rated V_{P1} (1 - D); I_{P1} at V_{P1} = 80 \% rated V_{P1} (1 - D); I_{P1} at V_{P1} = 80 \% rated V_{P1} (1 - D); I_{P1} at V_{P1} = 80 \% rated V_{P1} (1 - D); I_{P1} at V_{P1} = 80 \% rated V_{P1} (1 - D); I_{P1} at V_{P1} = 80 \% rated V_{P1} (1 - D); I_{P1} at V_{P1} = 80 \% rated V_{P1} (1 - D); I_{P1} at V_{P1} = 80 \% rated V_{P1} (1 - D); I_{P1} at V_{P1} = 80 \% rated V_{P1} (1 - D); I_{P1} at V_{P1} = 80 \% rated V_{P1} (1 - D); I_{P1} at V_{P1} = 80 \% rated V_{P1} (1 - D); I_{P1} at V_{P1} = 80 \% rated V_{P1} (1 - D); I_{P1} at V_{P1} = 80 \% rated V_{P1} (1 - D); I_{P1} at V_{P1} = 80 \% rated V_{P1} (1 - D); I_{P1} at V_{P1} = 80 \% rated V_{P1} (1 - D); I_{P1} at V_{P1} = 80 \% rated V_{P1} (1 - D); I_{P1} at V_{P1} (1 - D); I_{P1} (1$

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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS		
Dimensions http://www.vishay.com/doc?95018		
Part marking information	http://www.vishay.com/doc?95029	
Packaging information	http://www.vishay.com/doc?95034	



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