

CeraDiode

SMD type, single, case size 0402, 0603 and 1003

Series/Type: CDS*C**GTA Ordering code: B725**D****A***

Date: 2007-02-20

Version:



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CDS*C**GTA

Reliable ESD protection of single lines

Description

Due to the ongoing miniaturization, today's electronic devices are more and more sensitive to electrostatic discharges (ESD). Therefore reliable protection components become absolutely necessary to safeguard your valuable electronics against the impact of ESD.

CeraDiodes are ceramic semiconductors optimized specifically for high performance in ESD applications. They have a non-linear voltage/current characteristic for effectively suppressing extremely fast voltage transients and offer superior parametric stability over the complete operating range of –40 °C to +85 °C.

CeraDiodes are bi-directional devices. A single CeraDiode connected from signal/data line to ground routes both positive and negative ESD transitions safely to the ground plane. This technique eliminates the need to route ESD charge into the power plane, possibly damaging nearby integrated circuits.

Features

- Bi-directional ESD protection to IEC 61000-4-2 (level 4)
- Suitable for uni- and bi-directional lines
- Bi-directional ESD protection in a two-pin line
- Routes all ESD events, both positive and negative, safely to ground
- Suitable for DC working voltages up to 22 V
- No derating of maximum ratings up to 85 °C
- Surface mount package in case sizes

0402 (semiconductor package SOD-723)

0603 (semiconductor package SOD-523)

1003 (semiconductor package SOD-323)

- Extremely fast response time < 0.5 ns
- Lead-free nickel barrier terminations suitable for lead-free soldering
- RoHS-compatible

Applications

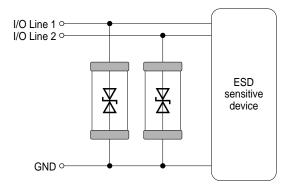
- EDP products (e.g. desktop and notebook computers)
- Peripherals (e.g. printers, memory cards, etc.)
- Portable handheld products (e.g. PDA)
- Mobile communication
- Consumer products (Flat TVs, set top boxes, MP3 players, digital cameras, etc.)
- Liquid crystal displays (LCD) / monitors



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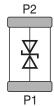
Application example



Note: Protection of I/O lines with single CeraDiodes.

Pin configuration

Pin	Description
P1	GND
P2	I/O Line



Due to the symmetrical configuration no marking information is needed. P1 and P2 can be interchanged.

Maximum ratings ($T_A = 85$ °C)

Rating	Symbol	Value	Unit
Maximum DC working voltage	V_{DC}	CDS2C05GTA: 5.6	V
		CDS2C15GTA: 15	
		CDS3C05GTA: 5.6	
		CDS3C09GTA: 9	
		CDS3C15GTA: 15	
		CDS3C20GTA: 22	
		CDS4C12GTA: 12	
Peak pulse current @ 8/20 µs	I _{PP}	CDS2C05GTA: 10	Α
		CDS2C15GTA: 10	
		CDS3C05GTA: 30	
		CDS3C09GTA: 30	
		CDS3C15GTA: 30	
		CDS3C20GTA: 30	
		CDS4C12GTA: 20	

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Rating	Symbol	Value	Unit
Peak pulse power @ 8/20 µs	P_{PP}	CDS2C05GTA: 320	W
		CDS2C15GTA: 670	
		CDS3C05GTA: 1000	
		CDS3C09GTA: 1600	
		CDS3C15GTA: 2000	
		CDS3C20GTA: 2200	
		CDS4C12GTA: 1000	
Air discharge ESD capability	V_{ESD}	15	kV
(to IEC 61000-4-2 method)			
Contact discharge ESD capability	V_{ESD}	8	kV
(to IEC 61000-4-2 method)			
Operating temperature	T _{op}	-40 to +85	°C
(without derating)			
Storage temperature	T _{stg}	-40 to +125	°C

Characteristics (T_A = 25 °C)

CDS2C05GTA

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Unit
Breakdown voltage	V_{BR}	$I_{BR} = 1 \text{ mA}$	6.4	-	-	V
Leakage current	I _{leak}	$V_{leak} = 3.3 \text{ V}$	-	-	1	μΑ
Clamping voltage	V_{clamp}	$I_{PP} = 1 \text{ A}, 8/20 \mu\text{s}$	-	-	24	V
Capacitance	С	V = 1 V, f = 1 kHz	-	180	-	pF

CDS2C15GTA

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Unit
Breakdown voltage	V_{BR}	$I_{BR} = 1 \text{ mA}$	20	ı	-	V
Leakage current	I _{leak}	$V_{leak} = 5.6 V$	-	-	1	μA
Clamping voltage	V_{clamp}	$I_{PP} = 1 \text{ A}, 8/20 \mu\text{s}$	-	-	46	V
Capacitance	С	V = 1 V, f = 1 MHz	-	47	-	pF

CDS3C05GTA

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Unit
Breakdown voltage	V_{BR}	$I_{BR} = 1 \text{ mA}$	6.4	-	-	V
Leakage current	I _{leak}	$V_{leak} = 3.3 \text{ V}$	-	-	1	μΑ
Clamping voltage	V_{clamp}	$I_{PP} = 1 \text{ A}, 8/20 \mu\text{s}$	-	-	19	V
Capacitance	С	V = 1 V, f = 1 kHz	-	470	-	pF

CDS3C09GTA

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Unit
Breakdown voltage	V_{BR}	$I_{BR} = 1 \text{ mA}$	10	-	-	V
Leakage current	I _{leak}	$V_{leak} = 5.6 \text{ V}$	-	-	1	μΑ
Clamping voltage	V_{clamp}	$I_{PP} = 1 \text{ A}, 8/20 \mu\text{s}$	-	-	30	V
Capacitance	С	V = 1 V, f = 1 kHz	-	220	-	pF

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CDS3C15GTA

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Unit
Breakdown voltage	V_{BR}	$I_{BR} = 1 \text{ mA}$	22	-	-	V
Leakage current	I _{leak}	$V_{leak} = 5.6 \text{ V}$	-	-	1	μA
Clamping voltage	V_{clamp}	$I_{PP} = 1 \text{ A}, 8/20 \ \mu \text{s}$	-	-	42	V
Capacitance	С	V = 1 V, f = 1 kHz	-	160	-	pF

CDS3C20GTA

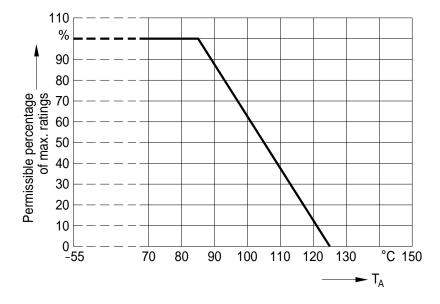
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Unit
Breakdown voltage	V_{BR}	$I_{BR} = 1 \text{ mA}$	25	-	-	V
Leakage current	I _{leak}	$V_{leak} = 5.6 \text{ V}$	-	-	1	μΑ
Clamping voltage	V_{clamp}	$I_{PP} = 1 \text{ A}, 8/20 \ \mu \text{s}$	-	-	50	V
Capacitance	С	V = 1 V, f = 1 MHz	-	56	-	pF

CDS4C12GTA

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Unit
Breakdown voltage	V_{BR}	$I_{BR} = 1 \text{ mA}$	16	-	-	V
Leakage current	I _{leak}	$V_{leak} = 5.6 \text{ V}$	-	-	1	μA
Clamping voltage	V_{clamp}	$I_{PP} = 1 \text{ A}, 8/20 \mu\text{s}$	-	-	46	V
Capacitance	С	V = 1 V, f = 1 MHz	-	82	-	pF

Note: Any operating voltage lower than V_{leak} results in lower leakage current.

Typical characteristics





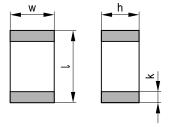
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Dimensional drawing

Dimensions in mm

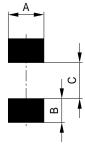
	0402		0603		1003	
Symbol	Min.	Max.	Min.	Max.	Min.	Max.
I	0.85	1.15	1.45	1.75	2.34	2.74
W	0.4	0.6	0.7	0.9	0.7	0.9
h	0.4	0.6	0.7	0.9	0.7	0.9
k	0.1	0.3	0.1	0.4	0.13	0.75



Recommended solder pad

Dimensions in mm

	0402	0603	1003
Symbol	Dim.	Dim.	Dim.
Α	0.6	1.0	0.8
В	0.6	1.0	0.8
С	0.5	1.0	1.45

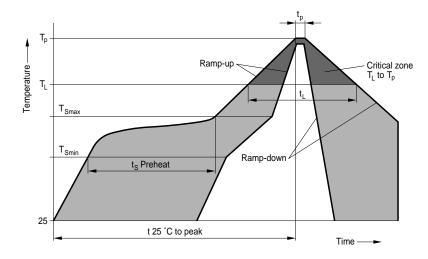




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Recommended infrared reflow soldering temperature profile



Profile feature	Sn-Pb eutectic assembly	Pb-free assembly
Average ramp-up rate (T _{Smax} to	3 °C/ second max.	3 °C/ second max.
T_p)		
Preheat		
 Temperature min (T_{Smin}) 	100 °C	150 °C
 Temperature max (T_{Smax}) 	150 °C	200 °C
- Time (t _{Smin} to t _{Smax})	60 120 seconds	60 180 seconds
Time maintained above		
 Temperature min (T_L) 	183 °C	217 °C
- Time (t _L)	60 150 seconds	60 150 seconds
Peak classification temperature	220 °C 240 °C	240 °C 260 °C
(T_p)		
Time within 5 °C of actual peak	10 30 seconds	20 40 seconds
temperature (t _p)		
Ramp-down rate	6 °C/ second max.	6 °C/ second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note: All temperatures refer to topside of the package, measured on the package body surface.

Soldering guidelines

The usage of mild, non-activated fluxes for soldering is recommended, as well as proper cleaning of the PCB.

The components are suitable for reflow soldering to JEDEC J-STD-020C.



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Storage conditions

As far as possible, the components shall be employed within 12 months. They should be left in their original packing to avoid soldering problems due to oxidized contacts.

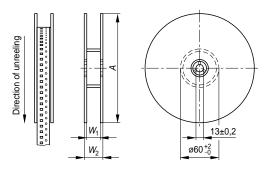
Storage temperature: -25 °C up to 45 °C

Relative humidity: < 75 % annual average, < 95 % on max. 30 days in a year.

Reel dimensions in mm

Definition	Symbol	Dim.	Tolerance
Reel diameter	Α	180	+0/ -3
Reel width (inside)	W ₁	8.4	+1.5/ -0
Reel width (outside)	W ₂	14.4	max.

Package: 8-mm tape Reel material: Plastic



Selection guide

Part number	Ordering code	Case size	Qty. per reel	Reel size
CDS2C05GTA	B72590D0050A060	0402	10000	180 mm
CDS2C15GTA	B72590D0150A060	0402	10000	180 mm
CDS3C05GTA	B72500D0050A060	0603	4000	180 mm
CDS3C09GTA	B72500D0090A060	0603	4000	180 mm
CDS3C15GTA	B72500D0150A060	0603	4000	180 mm
CDS3C20GTA	B72500D0200A060	0603	4000	180 mm
CDS4C12GTA	B72570D0120A060	1003	4000	180 mm

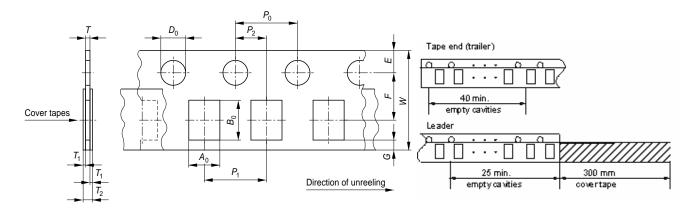


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Taping to IEC 60286-3

Tape material: Cardboard



Dimensions and tolerances in mm:

		0402	0603	1003	
Definition	Symbol	Dim.	Dim.	Dim.	Tolerance
Compartment width	A_0	0.6	0.95	1.0	± 0.2
Compartment length	B ₀	1.15	1.8	2.85	± 0.2
Sprocket hole diameter	D_0	1.5	1.5	1.5	+0.1/ -0
Sprocket hole pitch	P_0	4.0	4.0	4.0	± 0.1 1)
Distance centre hole to centre	P ₂	2.0	2.0	2.0	± 0.05
compartment					
Pitch of the component	P ₁	2.0	4.0	4.0	± 0.1
compartments					
Tape width	W	8.0	8.0	8.0	± 0.3
Distance edge to centre of hole	Е	1.75	1.75	1.75	± 0.1
Distance centre hole to centre	F	3.5	3.5	3.5	± 0.05
compartment					
Distance compartment to edge	G	0.75	0.75	0.75	min.
Thickness tape	T	0.6	0.9	1.0	max.
Overall thickness	T ₂	0.7	1.1	1.1	max.
4) 00 10 10 10 10	•	•			

¹⁾ \leq ± 0.2 mm over any 10 pitches

Note

CeraDiodes are not suitable for switching applications or for voltage stabilization, where static power dissipation is required.

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